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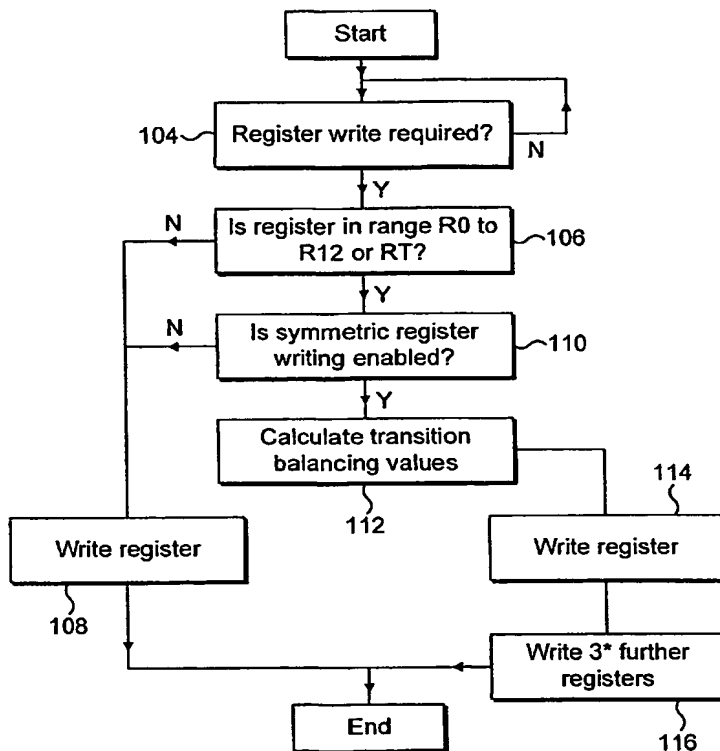
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(54) Title: **PROCESSING ACTIVITY MASKING IN A DATA PROCESSING SYSTEM**



(57) Abstract: A data processing system is provided with mechanisms such that when a data value is stored within a data register, further data values are stored within one or more further registers such that the total number of signal transitions from high to low and from low to high does not vary in dependence upon the data value being written or the previous data value.



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## INTERNATIONAL SEARCH REPORT

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**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 G06F1/00

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F G07F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB, WPI Data

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99/67766 A (CRYPTOGRAPHY RESEARCH INC ; KOCHER PAUL C (US); JAFFE JOSHUA M (US); J) 29 December 1999 (1999-12-29)	1,7
A	page 7, line 15 - line 18 page 7, line 25 - page 8, line 4 page 8, line 11 - line 13	2-6,8-12
A	ALMANEI S: "PROTECTING SMART CARDS FROM POWER ANALYSIS ATTACKS" 'Online! 28 May 2002 (2002-05-28), XP001130984 Retrieved from the Internet: URL: <a href="http://islab.oregonstate.edu/koc/ece679/project/2002/almanei.pdf">http://islab.oregonstate.edu/koc/ece679/project/2002/almanei.pdf</a> 'retrieved on 2002-10-17! the whole document	1-12



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Information on patent family members

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			EP 1088295 A2	04-04-2001
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